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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Confirmation No.: 6437
Alberto BARONCELLI et al Art Unit: 2673
S. N. 10/601,274 Examiner:
Filed: June 19, 2003
For: VECTOR GRAPHICS CIRCUIT ACCELERATOR FOR DISPLAY SYSTEMS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant/Applicants wishes/wish to draw attention to the documents listed on the accompanying Form PTO-1449, copies of which documents are enclosed.

Pursuant to MPEP 609, it is requested that the Examiner should initial Form PTO-1449 to confirm that each document has been considered, and return a copy of the form to the undersigned.

The documents were cited in an International Search Report, a copy thereof being attached.

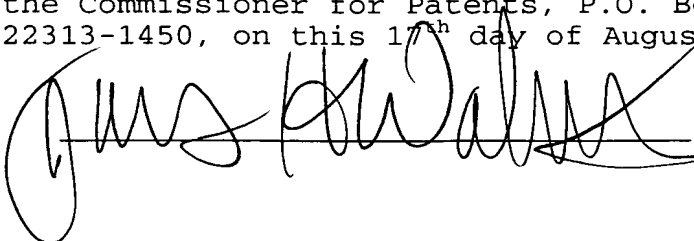
Respectfully submitted,

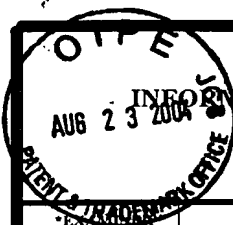

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I hereby certify that this correspondence is being deposited as first class mail with the United States Postal Service in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 17th day of August, 2004.





INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Docket Number (Optional) B-322	Application Number 10/601,274
		Applicant(s) Alberto BARONCELLI et al	
		Filing Date 06/19/03	Group Art Unit 2673
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
INITIAL		Foley, JD, Van Dam A et al: "Computer Graphics Principles and Practice" 1990, Computer Graphics Principles and Practice, Reading, Addison Wesley, US XP002272830, Page 507-510, Page 754-756, Page 855-887, Page 907-913	
		Watanabe T et al: "3-D CG media chip: an experimental single-chip architecture for three-dimensional computer graphics" IEICE Transactions On Electronics, Dec. 1994, Japan, vol E77-C, no. 12, pages 1881-1887, XP000497019 ISSN: 0916-8524, page 1881; figure 1	
		Cheng-Hsien Chen et al: "A cost effective lighting processor for 3D graphics application" Proceedings 1999 International Conference on Image Processing (CAT 99CH36348), Proceedings of 6th International Conference on Image Processing (ICIP'99), Kobe, Japan, 24-28 Oct. 1999, pages 792-796 vol. 2, XP010369032 1999, Piscataway, NJ, USA, IEEE, USA ISBN: 0-7803-5467-2 PAGE 792	
		PRYCE D: "Dual-port static RAMS. Specialized memories ease communications" EDN, 13 April 1989, USA, vol. 34, No. 8, pages 83-89, XP000117691, ISSN: 0012-7615	
		Chao P C et al: "A VLSI architecture for bicubic surface patch image generation" Eighth Annual International Phoenix Conference On Computers and Communications. 1989 Conference Proceedings (Cat. No. 89CH2713-6), Scottsdale, AZ USA 22-24 March 1989, pages 54-58, XP010015236 1989, Washington, DC, USA, Ieee Comput. Soc. Press, USA, ISBN: 0-8186-1918-x, page 54-55; Figure 1.	
EXAMINER		DATE CONSIDERED	

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.